



PATENT
P55281

IN THE CLAIMS

Please amend claims 1 through 8, and add new claims 9 and 10, as follows:

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1 (amended). ~~A flat panel display apparatus for receiving display information~~
including video data and synchronizing data from a host processing digital data in a
serial digital communication, said display apparatus adapted for operation without
need for any analog-to-digital converter (ADC) or phase-locked loop (PLL) circuit
for signal conversion, said display apparatus comprising:

a receiver for reconstructing said display information;

a synchronizing signal generator for generating a synchronizing signal by
extracting the synchronizing data from said reconstructed display
information;

a digital-to-analog converter (DAC) for converting said video data to a
corresponding analog video signal; and

an output terminal for externally transferring said synchronizing signal and
analog video signal to an analog display apparatus.

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2 (amended). ~~[A flat panel display as defined in] The display apparatus of~~
claim 1, further comprising a video data converter for converting line and dot
numbers of said video data so as to correspond to a prescribed display mode when
said synchronizing data has a different characteristic from said prescribed display
mode, and said synchronizing signal generator generates said synchronizing signal

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6 ~~corresponding to said display mode.~~

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1 3 (amended). A digital data processing [device used in] system including a
2 host computer for processing digital data and a flat panel display apparatus for
3 displaying display information received from [a] said host computer [processing
4 digital data], said system comprising:

5 a transmitter connected to said host to transfer digital display information as
6 serial data;

7 a receiver for reconstructing said digital display information;

8 a synchronizing signal generator for generating a synchronizing signal by
9 extracting synchronizing data from said reconstructed display
10 information;

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cont
11 a digital-to-analog converter (DAC) for converting video data to a
12 corresponding analog video signal; and

13 an output terminal for externally transferring said synchronizing signal and
14 said analog video signal to an analog display apparatus; [said flat panel
15 display including said receiver, synchronizing signal generator and
16 output terminal]

17 wherein said flat panel display apparatus includes said receiver, said synchronizing
18 signal generator, and said output terminal; and

19 wherein said flat panel display apparatus does not utilize any analog-to-digital
20 * converter (ADC) or phase-locked loop (PLL) circuit for signal conversion.

1 4 (amended). [A flat panel display as defined in] The display apparatus of
2 claim 2, further comprising:

3 a liquid crystal display (LCD) driver for receiving data output from said video
4 data converter; and

5 a [liquid crystal display (LCD)] LCD display panel for receiving an output
6 from said LCD driver.

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1 5 (amended). [A flat panel display as defined in] The display apparatus of
2 claim 1, said analog display apparatus comprising:

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3 an amplifier for receiving said video signal from said DAC via said output
4 terminal and amplifying said video signal;

5 a deflection signal generator for receiving said synchronizing signal output
6 from said synchronizing signal generator via said output terminal and for
7 generating deflection signals;

8 a high voltage generator for receiving an output from said deflection signal
9 generator and generating a high voltage;

10 a cathode ray tube (CRT) display for receiving said amplified video signal
11 from said amplifier and output signals from said deflection signal
12 generator and a high voltage from said high voltage generator.

1 6 (amended). [A digital data processing device used in a flat panel display as

2 defined in] The system of claim 3, [further] said system comprising a video data
3 converter for converting line and dot numbers of said video data [so as] to correspond
4 to a prescribed display mode when said synchronizing data has a different
5 characteristic from said prescribed display mode, and said synchronizing signal
6 generator generates said synchronizing signal corresponding to said display mode.

2 7 (amended). [A digital data processing device used in a flat panel display as
defined in] The system of claim 6, further comprising:

3 a liquid crystal display (LCD) driver for receiving data output from said video
4 data converter; and

5 a [liquid crystal display (LCD)] LCD display panel for receiving an output
6 from said LCD driver.

1 8 (amended). [A digital data processing device used in a flat panel display as
2 defined in] The system of claim 3, said analog display apparatus comprising:

3 an amplifier for receiving said video signal from said DAC via said output
4 terminal and amplifying said video signal;

5 a deflection signal generator for receiving said synchronizing signal output
6 from aid synchronizing signal generator via said output terminal and for generating
7 deflection signals;

8 a high voltage generator for receiving an output from said deflection signal
9 generator and generating a high voltage;

10 a cathode ray tube (CRT) display for receiving said amplified video signal from
11 said amplifier and output signals from said deflection signal generator and a high
12 voltage from said high voltage generator.

1 --9. In a flat panel display apparatus comprising:
2 a receiver means for reconstructing video display information including video
3 synchronization data from a host; and
4 a conversion means for converting said data to a corresponding video signal;
5 *the improvement comprising:*
6 a means for converting said data to a corresponding video signal without
7 utilization of an analog-to-digital converter (ADC) or a phase-locked
8 loop (PLL) circuit.

1 --10. In a method of processing display information containing video data and
2 synchronizing data from a host processing digital data in a serial communication, said
3 method comprising the steps of:

- 4 (1) reconstructing said display information to provide reconstructed display
5 information;
6 (2) generating a synchronizing signal by extracting the synchronizing data from
7 said reconstructed display information;
8 (3) converting said video data to a corresponding video signal; and
9 (4) transferring said synchronizing signal and video signal to a display;

10 *the improvement comprising:* a step for converting said video data to a corresponding
11 signal without utilizing an analog-to-digital converter (ADC) or phase-locked
12 loop (PLL) circuit.
